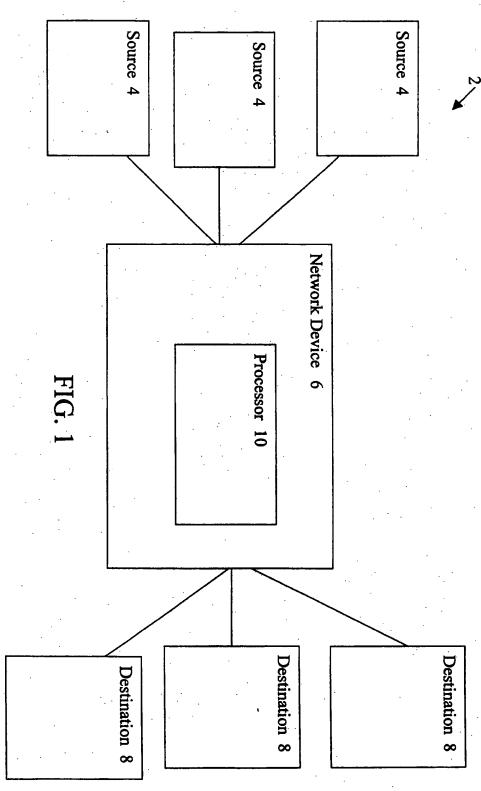


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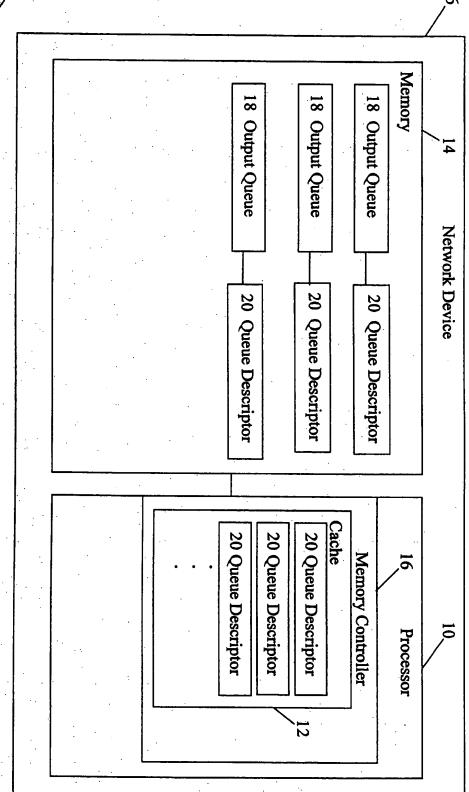


FIG. 2

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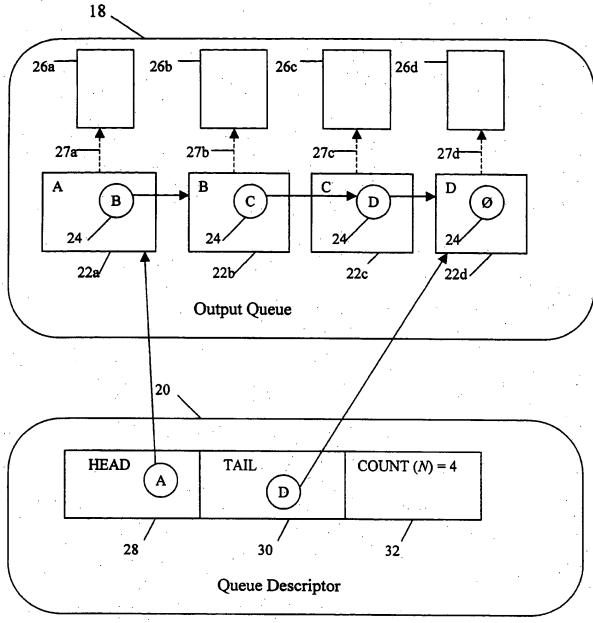


FIG. 3

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Cache

20 Queue Descriptor

34 Head Pointer Valid Bit

36 Tail Pointer Valid Bit

36 Tail Pointer Valid Bit

36 Tail Pointer Valid Bit

37 Head Pointer Valid Bit

38 Head Pointer Valid Bit

39 A Head Pointer Valid Bit

30 Queue Descriptor

30 Tail Pointer Valid Bit

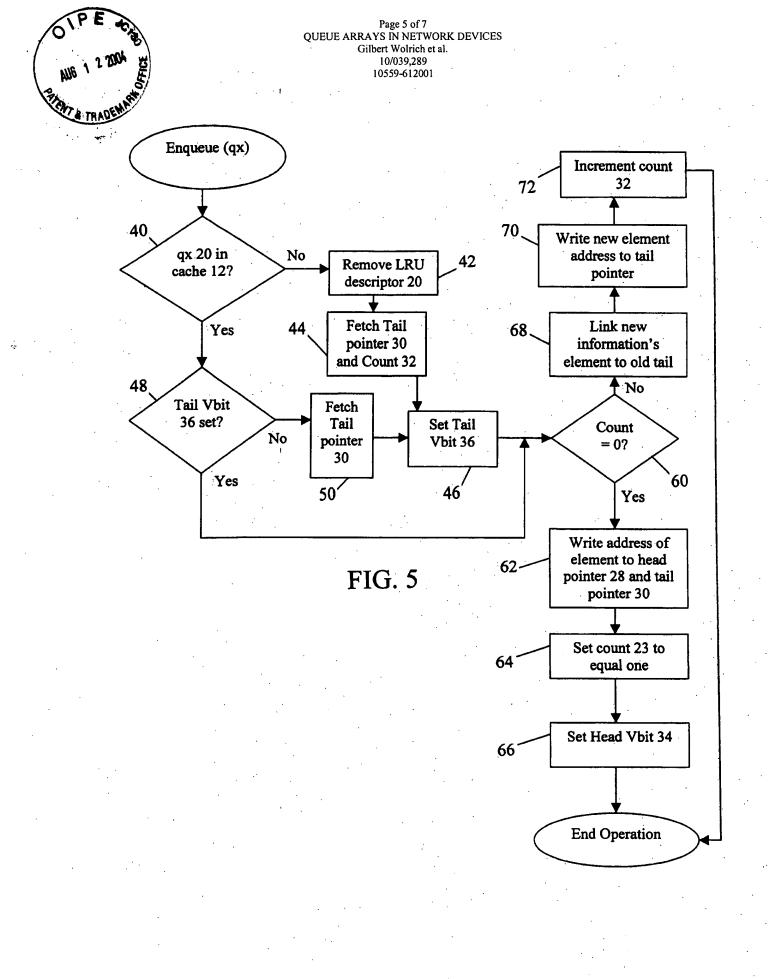
31 Head Pointer Valid Bit

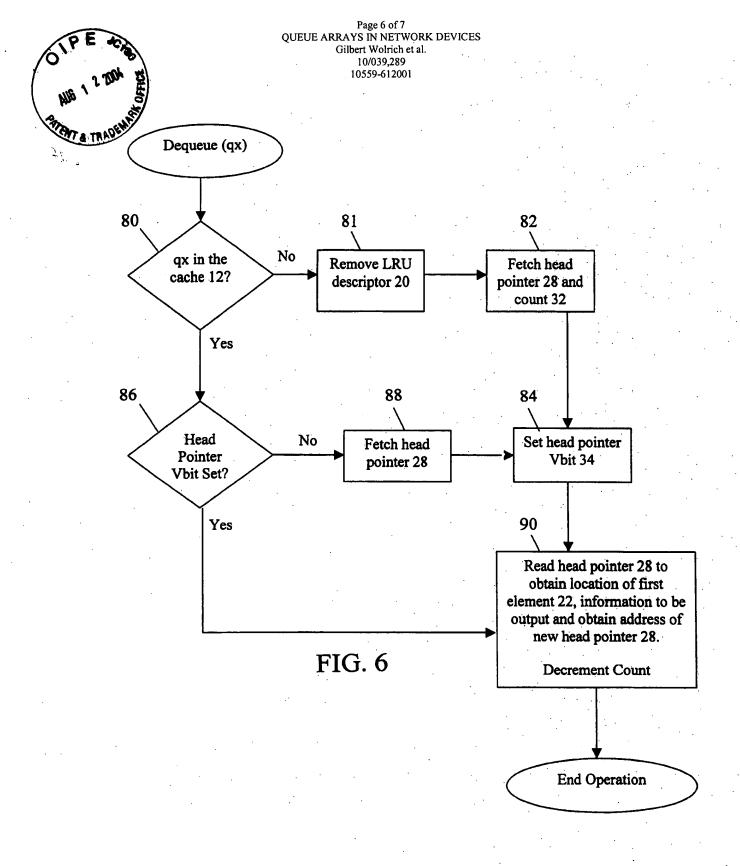
32 A Head Pointer Valid Bit

33 A Head Pointer Valid Bit

34 Head Pointer Valid Bit

FIG. 4







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